

Department of Electronics & Communication Engineering

Details of Research Papers in Conference

Sr. No.	Name of Faculty	Paper Name	Name of Conference/ Impact Factor/ISBN/ISSN
1.1	Dr. S. L. Haridas	Design of High Performance IEEE 754 Floating point multiplier using Vedic Mathematics	ICCICN-20 14,JRN RVP university,Udaipur,2014
1.2	Dr. S. L. Haridas	Designing challenges to Utilize white-Space for Configurable Femtocell Base on Slotted TDM	IC ³ I 2014,SJCE,Musuru, 2014
1.3	Dr. S. L. Haridas	Design of Quaternary adder with review of multiplier for high speed applications	3 rd International conference on Emerging trends & Research in Engineering & technology,Ameravti
1.4	Dr. S. L. Haridas	Review of Low power asynchronous viterbi decoder	International conference on computer & communication & informatics, 2013Coimbatore, 2013
1.5	Dr. S. L. Haridas	Comparison of Hard output and soft computing Viterbi Algorithm Decoding Techniques.	ICWET, 2011
1.6	Dr. S. L. Haridas	Soft Output Viterbi Decoder Using Hybrid Register Exchange	ICWET/2011/Mumbai
1.7	Dr. S. L. Haridas	A review on Intelligent Based Threat prevention Engine Architecture	ADVICE- Feb2010
1.8	Dr. S. L. Haridas	FPGA Implementation of Viterbi Decoder with Minimum Transition Register Exchange Processing	ICETET 2010
1.9	Dr. S. L. Haridas	Design of Viterbi Decoder with Minimum Transition Hybrid Register Exchange Processing	ICWET/2010/Mumbai
1.10	Dr. S. L. Haridas	Design of Viterbi Decoder using Minimum Transition Register Exchange Method	ICSSA, BDCOE, Dec 2009, Gujrat Vidhyanagar

1.11	Dr. S. L. Haridas	Design of Viterbhi Decoder with modified Traceback & Hybrid Register Exchang processing	ICAC3, Bandra, Mumbai, 2009
1.12	Dr. S. L. Haridas	Software Simulation Of Driverless Vehicle using Expert System	TIME/118/A-11, Feb 2004
1.13	Dr. S. L. Haridas	New Millenium Challenge Path For Process of Private Technical Institute	2000
1.14	Dr. S. L. Haridas	Trunking- An Option	April 1999
2.1	Dr Pankaj Agrawal	Cumulative Video Analysis Based Smart Framework for Depression Disorders	ICPC-2015 Pune,.
2.2	Dr Pankaj Agrawal	Electrocardiogram Based Depression Analysis Using Expert Opinion	ICRTET-2014, Nashik, March2014
2.3	Dr Pankaj Agrawal	Acute direction route node selection multipath routing for VANET: Design approach	ICSPIN,2014
2.4	Dr Pankaj Agrawal	Multipath Reliable Range Node Selection Distance Vector Routing for VANET: Design Approach	ICESC-2014 Nagpur
2.5	Dr Pankaj Agrawal	Design of Phase Frequency Detector and Charge Pump for Low Voltage High Frequency PLL	ICESC-2014,Nagpur
2.6	Dr Pankaj Agrawal	Fast Frequency Acquisition Phase Frequency Detector with minimal dead Zone for high frequency phse lock loop	
2.7	Dr Pankaj Agrawal	Clinical Depression analysis Using Speech Features	ICETET-13, Nagpur,
2.8	Dr Pankaj Agrawal	Routing Algorithms for NoC Architecture a Relative Analysis	ICETET-13, Nagpur, India
2.9	Dr Pankaj Agrawal	Modelling of CGRA to Improve Power Efficiency for Computationally Intensive Application	ICETET-13, Nagpur. India
2.10	Dr Pankaj Agrawal	An Efficient Virtual Channel Router for NoC's	IEMCON 2013, Kolkatta.
2.11	Dr Pankaj Agrawal	Comparison of K-means and Adaptive K-means using Matlab Simulation	ICACCN-2011, Chandigarh, India,

2.12	Dr Pankaj Agrawal	Comparison of K-means and Adaptive K-means with VHDL Implementation	IEMCON 2011, Kolkatta,
2.13	Dr Pankaj Agrawal	MATLAB implementation of image segmentation algorithms	ICCSIT, 2010, Chengdu, China,
2.14	Dr Pankaj Agrawal	Study of Various Edge Based Image Segmentation Methods	ICISN, 2009
2.15	Dr Pankaj Agrawal	Classification of Various Image Segmentation Methods	NCMIPMV '08, Chennai, Dec 2008
2.16	Dr Pankaj Agrawal	Copled Markov Random Field Model For Segmentation Of Textured Image	NCECA, 2008
2.17	Dr Pankaj Agrawal	Cellular Technologies Beyond 3G	
3.1	Prof. Vibha Bora	A Heuristic Approach for Automated Mammogram Orientation in Computer Aided Diagnosis.	ICETET- Nov 15, Japan.
3.2	Prof. Vibha Bora	MRI Brain Cancer Classification Using Support Vector Machine	2014 IEEE students Confernce on Electrical,Electronics and Computer Science, MANIT, Bhopal 978-1-4799-2526-1, March 2014
	Prof. Vibha Bora	Brain Cancer Discrimination using Hybrid support Vector Machine Review	RTITC-13, ISBN978-93-5137-585-2 Nov 2013
3.3	Prof. Vibha Bora	Tumor Segmentation by Tolerance Near Set Approach in Mammography and Lession Classification with Neural Network	ICCICT, Mumbai (presented), Oct 2012
3.4	Prof. Vibha Bora	Mammogram Segmentation by Near Set Approach and Mass Lesions Classification with Rough Neural Network	International conference on Contemporary issues CIAS, Indian Statistical Institute ,Kolkata.2012
3.5	Prof. Vibha Bora	Motion Vector Estimation of Video Image by Pyramidal Implementation of Lucas Kanade Optical Flow	IEEE conference ICETET 2009, pp. 914 – 917 10.1109/ICETET.2009.154 Citation: 32,2009
3.6	Prof. Vibha Bora	Motion Estimation Algorithm in Video Coding	KES ,Italy.2007
4.1	Prof. Vipin Bhure	Design of digital series FIR filter for power optimization	Green computing and Internet of Things (ICGCIoT),2015 International conference Publisher: IEEE, Oct 2015



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4.2	Prof. Vipin Bhure	Design of High Throughput and area efficient advanced Encryption system Core	International conference on Communication and signal processing, April 2014
4.3	Prof. Vipin Bhure	Level Set Method for Simultaneous Segmentation and Bias Correction for Intensity Inhomegenity In MRI	ICGTI/, Bhandara, March 2014
4.4	Prof. Vipin Bhure	Design Of Cache Controller for Muti-core systems Using Multilevel Scheduling Method	Fifth international conference on Emerging trends in Engineering & technology, 2012
5.1	Prof. Shubhangini Ugale	An Efficient AODV Routing Protocol for Cognitive Ad Hoc Networks	NCITCE/Yavatmal
5.2	Prof. Shubhangini Ugale	AODV Routing Protocol for Cognitive Adhoc Network	ICORT//Pune
6.1	Prof.Sanjay Tembhumne	Design a Vedic Divider using Binary Vedic Shifting Method for Low power Arithmetic Unit	ICQUEST-2015
6.2	Prof.Sanjay Tembhumne	Design of High Speed Sigma Delta Modulator for high wireless application	International conference,TECHO TRONICS-15
6.3	Prof.Sanjay Tembhumne	Implementation of Amplitude Modulation and Demodulation Using Analog Multiplier For 2.4GHz to 2.5GHz	ICETET-2012,Japan
6.4	Prof.Sanjay Tembhumne	VLsi Approach Fro Four Quadrant Analog Multiplier For 2.4Ghz to 2.5Ghz	ITC 2012,LNEE,pp.441-4444,2012,spinger-Verlag Berlin Heidelberg2012
7.1	Prof. Amol Boke	Design and Implementation of Deadline based EDF Algorithm on ARM LPC 2148	
8.1	Prof. Anjali Khare	A Low Voltage Wideband CMOS Operational Transconductance Amplifire For VHF Application	ICESC, 2014, IEEE, INSPEC:14117536, Jan 2014
9.1	Prof. Prachee Mersh	Color Image Segmentation by Using RGB Histogram	International Conference on Advancement in engineering & Management (Conference), 2012
10.1	Prof. Anup Nage	Removal of stiction Mechanism in Micro Mechanical Switch	NCVDES/Nagpur

