

## Department of Electronics & Communication Engineering Details of Research Papers in Journals

Sr. No.	Name of Faculty	Name of Publish Paper	Name of Journals /Volume No/Issue No/Pg. No/ISBN/ISSN/I.P
1.1	Dr. S.L.Haridas	Delay Optimizaton in CORDIC Waveform Generator	JETIR (ISSN-2349-5162), Vol-3, Issue 6, June 2016
1.2	Dr. S.L.Haridas	Design of SIMO module for white space utilization under ISM band: review analysis.	IJLTET, Vol 7, ISSN 2278621X, May 2016
1.3	Dr. S.L.Haridas	Object tracking using modified mean shift algorithm in a robust manner	IJSTE, Vol 2, issue 1, July 2015
1.4	Dr. S.L.Haridas	Face recognition by using gabor Wavelet, Rough set theory and KNN Classifier	IJSRD, ISSN 2321-0613, Vol 3 issue 4, May 2015
1.5	Dr. S.L.Haridas	Design of the Physical layer of PCI express using VHDL	IJSRD, Vol.3 Issue 05, ISSN:121-124, May 2015
1.6	Dr. S.L.Haridas	Object tracking in real time video using mean shift algorithm	IJSRD, ISSN 2221-0613, Vol, April 2015
1.7	Dr. S.L.Haridas	Design of Quaternary adder for high speed application	IJSTN, ISSN 2394-1537, Vol 4 issue 1, March 2015
1.8	Dr. S.L.Haridas	Review on floating point mupltiplier using ancient techniques.	IOSR-JEEE, ISSN:2320-3331, 2014
1.9	Dr. S.L.Haridas	Energy Efficient Automized Public Utility Building	IJARCSE, ISSN:2277 128X, Vol.4, Issue 5, 2014
1.10	Dr. S.L.Haridas	Real time Object tracking using mean shift algorithm- A review	ISSN ,2348-9928, Volume 1, Issue 7, Nov 2014
1.11	Dr. S.L.Haridas	Controlling Robot through SMS with Acknowledging facility	IOSR-JEEE, ISSN:2320-3331, Vol.9, Issue 3 ,May-June 2014
1.12	Dr. S.L.Haridas	Energy Efficient Automimized Bottling Plant Using PLC And SCADA With Speed Variable Conveyor Assembly	IOSR-JECE, ISSN:2278-8735, Vol.9, Issue 1, Jan 2014
1.13	Dr. S.L.Haridas	Acknowledging GSM Controlled Robot	IJERA, ISSN:2248-9622, April 2014
1.14	Dr. S.L.Haridas	Very Low Power Viterbi Decoder Employing Minimum Transition and Exchangeless Algorithms for Multimedia Mobile Communication	IJACSA/Vol.2, No.12, 2011

1.15	Dr. S.L.Haridas	A low power Viterbi decoder design with minimum transition hybrid register exchange processing for wireless applications	IJVLISCS/Vol.1/ Dec 2010
1.16	Dr. S.L.Haridas	A low power Viterbi decoder design using Hybrid register exchange processing for wireless applications	IJETIE,ISSN 0974-4975,Vol.03,Issue 1, April 2010-June 2010
2.1	Dr. Pankaj Agrawal	Design Issue and Proposed Implementation Communication Aid for Deaf & dumb People.	IJRITCC, ISSN:2321-8169, Vol:3, Issue. May 2015
2.2	Dr. Pankaj Agrawal	Power and Area Efficient NOC Router Through Utilization of Idle Buffers	IJCEA, Volume VI, Issue I, pp. 31-39, ISSN no: 2321-3469, April 2014
2.3	Dr. Pankaj Agrawal	Minimization of Latency and Power for Network-on-Chip	IJRITCC, vol. 3, Issue 2, ISSN no: 2321-8169, Feb. 2015
2.4	Dr. Pankaj Agrawal	Implementation of Buffer for Network on Chip Router	IJTEL vol. 3, issue 1, pp. 362-365. ISSN no: 2319-2135, April 2014
2.5	Dr. Pankaj Agrawal	Effective Retransmission Reducing Ad hoc On Demand Multipath Distance Vector Routing Protocol for VANET	IJERGS, Vol. 3, Issue 1, pp. 1383-1388, ISSN 2091-2730, Jan- Feb. 2015
2.6	Dr. Pankaj Agrawal	Performance analysis of speculative virtual channel router for network-on-chip	European Scientific Institute, ESI, 2013, vol. 3, pp. 247-252, ISBN: 978-608-4642-15-2, Vol. 3, Dec. 2013
2.7	Dr. Pankaj Agrawal	Depression Analysis using ECG Signal	IJCT, vol. 11, no. 7, pp. 2746-2751, ISSN: 2277-3061, Nov 2013
2.8	Dr. Pankaj Agrawal	Efficient Method for Stress Detection and Analysis	IJREM, vol. 3, no. 3, pp. 309-315, ISSN: 2278-7690.
2.9	Dr. Pankaj Agrawal	Design and Implementation of low power Dickson Charge Pump in 0.18 $\mu$ m CMOS Process	IJSER, vol. 4, Issue 8, pp. 1941-1944, ISSN: 2229-5518, Aug 2013
2.10	Dr. Pankaj Agrawal	Performance Analysis of Router for Network on Chip	IJCET, vol. 3, no. 2, pp. 685-687, ISSN: 0974-3588, June 2013
2.11	Dr. Pankaj Agrawal	Comparison of K-means and Adaptive K-means using MATLAB Simulation	IJAEE, vol.1, issue 1, pp. 185-190, ISSN: 2278 - 215x, 2011.
2.12	Dr. Pankaj Agrawal	Study of Various Interactive Image Segmentation Methods	IJ-ETA-ETS, vol. 2, issue 2, pp. 825-830, ISSN: 0974-3588, July-Dec 2009
2.13	Dr. Pankaj Agrawal	Algorithmic review of Image Segmentation Methods	IJ-CA-ETS, vol. 1, issue 2, pp. 600-605, ISSN 0974-3596 April'2009.
3.1	Vibha Bora	Assesment Of Fruit Maturity Using Direct Color Mapping	IRJET Vol-3, Issue 03, ISSN:2395-0056, March 2016
3.2	Vibha Bora	Robust Automatic Pectoral Muscle Segmentation from Mammograms Using Texture Gradient and Euclidean Distance	Journal of Digital Imaging, Sci Indexed Journal. Springer Publisher DOI 10.1007/s10278-015-9813-5, Feb 2016

3.3	Vibha Bora	Automatic Segmentation of Cell Nuclei in Breast Histopathology Images and Classification using Feed Forward Neural Network	IJERA ,ISSN:2248-9622 Impact Factor 1.69 (By Google Scholar Citation Report) April 2014
3.4	Vibha Bora	"Spatial Fuzzy Clustering techniques for Automated MR Image Segmentation"	IJGTI, Volume3/Issue2, ISSN:2277-6591, March 2014
4.1	Prof.Vipin Bhure	Review paper on Parallel Processing Single Precision floating Point Multiplier based RISC Processor	ISSN-2277-4106, 12 March Vol 6, April 2016
4.2	Prof.Vipin Bhure	LMS Algorithm and distributed arithmetic based adaptive FIR filter with low area complexity	IJARCCCE, Vol.4, Issue 6, June 2015
4.3	Prof.Vipin Bhure	Mems Based different types of resonator	IJSER Vol6, issue 6 June-15, ISSN 2229-5518, June 2015
4.4	Prof.Vipin Bhure	Design and Analysis of Clammed Free MEMS Resonator	IJRITCC, ISSN 2231-8169 Vol 3 ,issue 6, June -2015
4.5	Prof.Vipin Bhure	Design And Analysis of digital PID Controller A Review.	JETIR ISSN-2349-, vol 2issue 4, April 2015
4.6	Prof.Vipin Bhure	Study of Differential Amplifier using CMOS	IJERGS Vol-3, ISSN 2091-2730, March-April2015
4.7	Prof.Vipin Bhure	Two stage OP-AMP using CMOS-AREVIEW	IJAICT, ISSN:2348-9928, Nov 2014
4.8	Prof.Vipin Bhure	Review on FFT processor for OFDM system	IJAICT, ISSN:2348-9928, Nov 2014
4.9	Prof.Vipin Bhure	Design of parallel CRC generation for high speed application	IJAICT, ISSN 2348-9928, Volume 1, Issue 7, Nov.2014
4.10	Prof.Vipin Bhure	Adaptive FIR Filter with high Throughput and Low power consumption	IJST, ISSN 2321-919X, Nov.2014
4.11	Prof.Vipin Bhure	Tissue Segmentation & Bias Correction in MRI using level set approach.	IJRA, ISSN2248-9622/12th-13th April 2014
4.12	Prof.Vipin Bhure	VLSI Architecture for Optimized Low Power Digit Serial FIR using MCM	IJERD, ISSN:2278-800X, July 2014
4.13	Prof.Vipin Bhure	Design of Cache Controller for Multi-core System by Multi Level Scheduling.	IJECT Vol-3, ISSN:2230-7109, July-Sept 2012
4.14	Prof.Vipin Bhure	Review on 2:4 DECODER BY REVERSIBLE LOGIC GATES FOR LOW POWER CONSUMPTION	ISSN:2395-0056, IRJET, Feb 2006
5.1	Prof.Anup Nage	A Non-Contact Type Comb Drive for the Removal of Stiction Mechanism in MEMS Switch	IJRTET, Vol.9, July 2013



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5.2	Prof. Anup Nage	Design Of IEEE 754 Format 32 Bit Complex floating Point Vedic Multiplier- A Review	JETIR ISSN-2349-5162 Vol 2, Issue 6, June 2015
5.3	Prof. Anup Nage	Design Of IEEE 754 Format 32 Bit Complex floating Point Vedic Multiplier	IJSRD ISSN 2321-0613 Vol 3, Issue 05, 2015
5.4	Prof. Anup Nage	A REVIEW ON HIGH SENSITIVE BIOSENSOR USING MEMS	IJAICT, Vol 1, Issue 7, ISSN 2348-9928, November 2014
6.1	Prof.Amol Boke	A Routing Algorithm For Non-Uniform Mesh Network on NoC	2016, IJEDR, Vol-4, issue 1, ISSN:2321-9939, 2016
6.2	Prof.Amol Boke	Designing Of Double Tail Comparator For High Speed Application: A Review	IRJET, Vol-3 Issue:04, ISSN:2395-0056, April 2016
6.3	Prof.Amol Boke	Review on Random Number Generator design using Quantum Dot Cellular Automata	IJIRT, Vol-2, Issue 9, ISSN: 2349-6002 Feb 2016
6.4	Prof.Amol Boke	Design and analyze power efficient BCD adder using digital logic technique	IJSRD, Vol 3, Issue 5, ISSN 2321-0613, Jan 2016
6.5	Prof.Amol Boke	RFID based real time password authentication system for ATM	IJSRD, ISSN 2321-0613, Vol 3 issue 4, April 2015
6.6	Prof.Amol Boke	Monitoring System for Frozen and Pharmaceutical Products during Transit using 1-Wire Protocol: Design and Implementation	IJSRD Vol-3, Issue 04, 2015, ISSN 2321-0613, 2015
6.7	Prof.Amol Boke	Design and Implementation of Monitoring system for frozen food during transportation using 1-wire protocol: A review	IJAICT, Vol.1, Issue 7, ,ISSN:2348-9928, Nov 2014
6.8	Prof.Amol Boke	Review paper on real time password authentication system for ATM	IJAICT, Vol.1, Issue 7, 2348-9928, Nov 2014
6.9	Prof.Amol Boke	Design and Analyze High Speed, Power Efficient Full Adder Using Digital Logic Technique	IJAICT, Vol.1, Issue 7, ,ISSN 2348-9928 Nov 2014
6.10	Prof.Amol Boke	A LEACH protocol for Wireless Sensor Network: A Review	IJCA/Vol.66, ISSN:0975-8887/March 2014
6.11	Prof.Amol Boke	Cellular Automata based Degree Priority Routing Algorithm for Irregular Mesh Topology NOCs	IJCA/Vol.66, ISSN:0975-8887/March 2013
7.1	Prof.Shubhangini Ugale	Route planning algorithm for localization in wireless sensor network	IJARCCCE, ISSN2278-1021, July 2015
7.2	Prof.Shubhangini Ugale	Design and Improve BER of LDPC decoder using advanced Gallager's algorithm	ICSIT, Vol.2, ISSN:2393-9915, April-June 2015
7.3	Prof.Shubhangini Ugale	Study of Tree Multiplier using Reversible Logic Gate	IJAICT, ISSN 2348-9928(O), Vol.1, Issue 7, Nov.2014

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7.4	Prof.Shubhangini Ugale	Design of the physical layer of PCI express – A review	IJAICT ,ISSN 2348-9928,Volume 1,Issue 7, Nov.2014
7.5	Prof.Shubhangini Ugale	Review on 128 bit advanced encryption standard algorithm with fault detection	IJAICT, ISSN 2348-9928,Volume 1,Issue 7, Nov.2014
7.6	Prof.Shubhangini Ugale	To analyse hand gesture recognition for wirelessly electronics device control	IOSR-JECE, ISSN:2278-8735, vol 9 issue 4. July-Aug 2014
7.7	Prof.Shubhangini Ugale	A methods for controlling congestion in VANET	IOSR-JECE, ISSN:2278-8735 vol 9, issue 4, July-Aug 2014
7.8	Prof.Shubhangini Ugale	Analysis Of Routing Protocol for Cognitive Adhoc Network	IJECCE, Vol-3 , Issue 2, ISSN 2249-071X, 2012
7.9	Prof.Shubhangini Ugale	Review paper on decoding of LDPC code using advanced Gallager's algorithm.	IJAICT,ISSN2348 - 9928 Volume 1 issue 7, Nov. 2004
8.1	Prof.Sanjay Tembhurne	Design of inter integrated circuit bus protocol and by using bist method- a review	IJSEAS,ISSN 2395 – 3470, July 2015
8.2	Prof.Sanjay Tembhurne	Design of inter integrated circuit with BIST Method	IJSR,Vol 4 issue 7 july 2015
8.3	Prof.Sanjay Tembhurne	A Review on low power design for asynchronous viterbi decoder	IJAICT,2348-9928,Volume 1,Issue 7, Nov.2014
8.4	Prof.Sanjay Tembhurne	Adaptive FIR Filter with high Throughput and Low power consumption	IJST,ISSN 2321-919X, Nov.2014
8.5	Prof.Sanjay Tembhurne	Design of parallel CRC generation for high speed application	IJAICT,ISSN 2348-9928,Volume 1,Issue 7, Nov.2014
8.6	Prof.Sanjay Tembhurne	Methods For the Minimization of Actuation Voltage In MEMS Switch	IJERA, ISSN:2248-9622 Vol-4,ISSUE 3, March 2014
8.7	Prof.Sanjay Tembhurne	VLSI Design Of Four Quadrant Analog Multiplier For ISM Band	ITC 2012,LNEE,pp.441-444,2012,Springer-Verlag Berlin Heidelberg, May 2012
9.1	Prof.Pragati Kene	Design of 64 Bit MAC Unit for Efficiency Improvement using VEDIC Multiplier	IJSRD,Vol 3,Issue 5,ISSN 2321-0613, May 2015
9.2	Prof.Pragati Kene	Design of effcient 64 BIT MAC Unit using VEDIC Multiplier for DSP Application :A Review	IJAICT,Vol.1,Issue 9, ,ISSN 2348-9928, Jan 2015
9.3	Prof.Pragati Kene	Design Of Secured Handoff Machnism for WiMAX & GPP LTE	IJERT, ISSN:2278-0181 vol-2, issue-2013, June2013
9.4	Prof.Pragati Kene	FLC Based Handoff Mechanism For for	IJETAE, Vol 3, Issue-2, ISSN:2250-2495, Feb 2013





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10.1	Prof. Anjali Khare	Effect OF Scaling On Operation Of Low Voltage OTA	IJES, Vol-2, Issue No 1, ISSN 2319-1813,2013
11.1	Prof. Prachee Mersh	Intensity Based Nuclei Segmentation of Cancer Cell	IJPRET, 2013; Volume 1(8):419-427 ISSN: 2319-507X , 01/04/2013
11.2	Prof. Prachee Mersh	IVRS for College Automation	ISSN (Print) : 2319 –2526, Volume-2, Issue-1, 2013,Page no. 90 to 94, Feb 2013
11.3	Prof. Prachee Mersh	A Novel Approach to Color Image Segmentation	Volume 2, Issue ICRAET12, ISSN Online: 2277-2677 Page no. 77 to 80, May 2012

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